FIG. 1

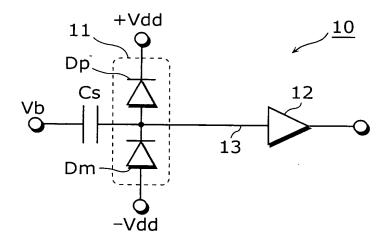


FIG. 2

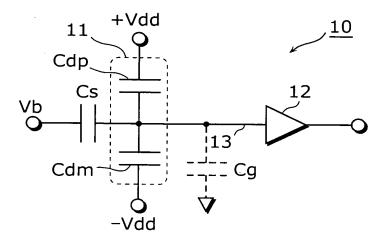


FIG. 3 <u> 20</u> +Vdd 21 Vh Dp1 21a Dp2 Rh Cs 12 Vb 1,3 Vout Dm1 21b Dm2 -Vdd

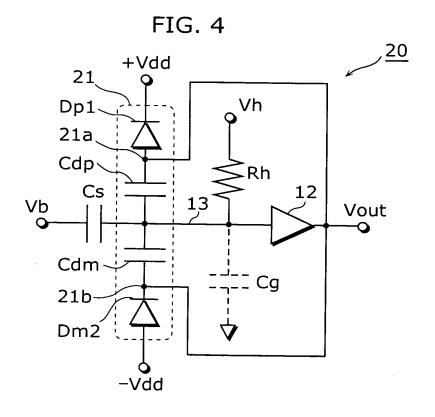
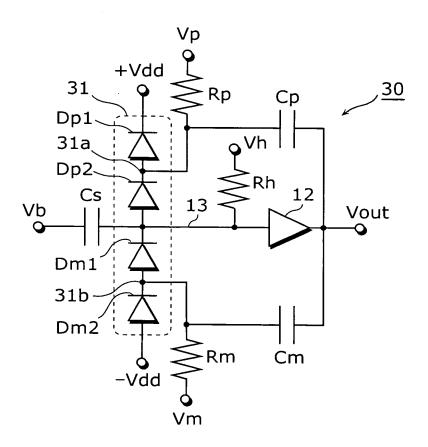
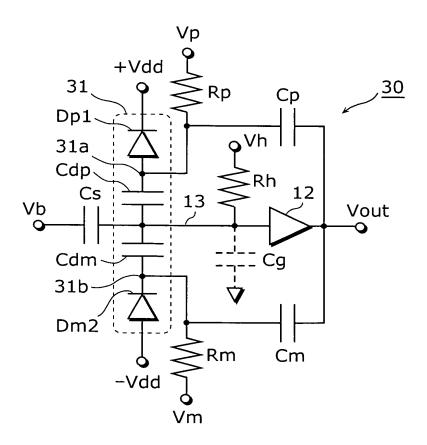


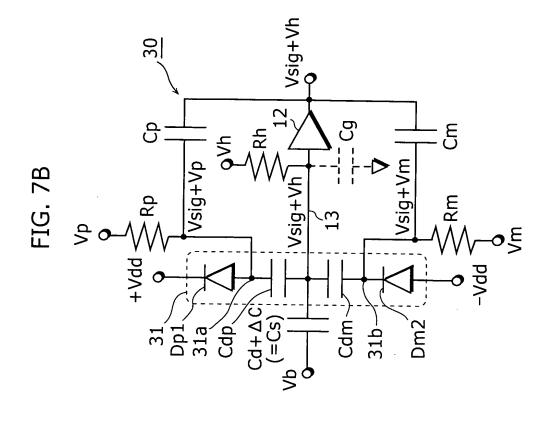
FIG. 5



(+Vdd≧Vp≧Vh≧Vm≧-Vdd)

FIG. 6





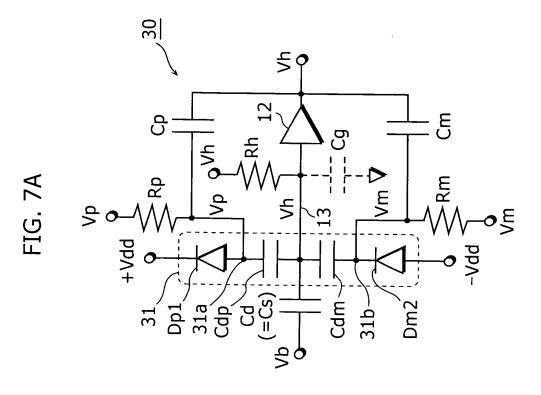


FIG. 8

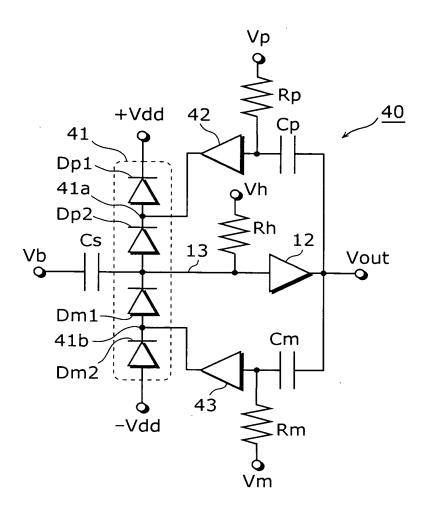


FIG. 9

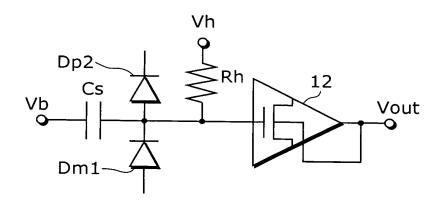


FIG. 10

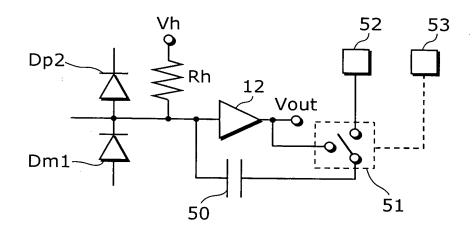
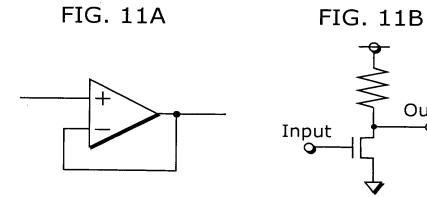


FIG. 11A



Output